

Design & Simulation of Circular Rectangular Microstrip Patch Antenna for Wireless Applications

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Abstract: Over the past one decade, there is a rapid growth in development of wireless communication applications. The performance of all such wireless systems depends on the design of the antenna. Microstrip antennas are preferred for majority of their applications and advantages. This paper presents the design and simulation of circular rectangular patch microstrip antenna. The overall antenna is designed and simulated in Ansoft High Frequency Structure Simulator (HFSS) Software. The substrate used in this configuration is Rogers RT/Duroid and substrate is used with $\varepsilon_r = 2.33$. The proposed antenna is used for various wireless communication applications. Gain, Return loss and bandwidth are the performance parameter of the proposed antenna. The proposed antenna is analyzed at resonant frequency of 16.6 GHz & 18.3 GHz. The result calculated is return loss of patch is -35.7673 dB & -29.2070 dB and gain of patch is 5.6925 dB & 12.44 dB respectively.

Keywords: Microstrip antenna, Miniaturized, Return Loss, Gain & Bandwidth, Automotive Radar Systems.

I. Introduction

In the present time, the enhanced technology of wireless communication is increase day by day as per the requirements. In prospective to security point of view, wireless communications are used in automatic organization and company. Wireless communication systems are used to transmit images and videos with higher data rates, so microstrip patch antenna is commonly used. Microstrip patch antenna becomes very popular day by day because of its ease of analysis and fabrication, low cost, light weight, easy to feed, capability of dual, triple and several frequency operations and their attractive radiation characteristics. Recently microstrip patch antennas have been widely used in satellite communications, aerospace, radars, biomedical applications and reflector feeds because of its inherent characteristics such as mechanically robust, compatibility with integrated circuits and very versatile in terms of resonant frequency, polarization, pattern and impedance. In spite of its several advantages of microstrip antenna, they suffer from drawbacks such as narrow bandwidth, low gain etc. These drawbacks limit their applications in other fields. In order to overcome the limitations of microstrip patch antennas, numerous techniques are proposed i.e. increasing the thickness of the dielectric substrate, decreasing dielectric constant and using different shapes of patch. There are many shapes of patch like circular, rectangular, triangular but circular and rectangular are most popular. In recent years there has been considerable effort in the antenna application to suppress the surface wave and overcome the limitations of the antenna. The purpose of this paper is to propose a microstrip antenna which will increase the bandwidth without increasing its physical dimensions [1-3]. However,

in some applications the small size conventional patch antenna is still too large, so research is still being focused on the miniaturization of the patch antenna over the years. In this paper, patch antenna design with compact size is one major consideration. In our studies the patch influenced the characteristics of the proposed antenna such as resonant frequency, bandwidth and radiation characteristics, etc.

The rest of paper is design as follows. The introduction of microstrip antenna is described in section I. The configuration & design of the proposed antenna and the parameters especially for substrate and patch are described in section 2. In Section 3, the result and discussion of proposed antenna compared with the conventional antenna are described. Finely the conclusion is described in section 4.

II. Antenna Configuration and Design

In this section, the design and analysis of the proposed antenna as shown in figure 1 is discussed. Patch antenna is simulated by High Frequency Structure Simulator (HFSS) software.

Circular Rectangular Patch Antenna Design

A circular rectangular patch microstrip antenna is designed and simulated here. The propose methodology of research work is to optimize design of antenna with proper feed. In proposed antenna coaxial feed technique is used. The firstly we design the substrate having dimension $40 \times 50 \times 0.8$ mm. The substrate has material Roger RT/Duroid and substrates is used with $\varepsilon_r = 2.33$ with 0.8 mm thickness. After designing the substrates, on the top side of the substrate, a radiating patch is printed with feeding points (0,-

"Design & Simulation of Circular Rectangular Microstrip Patch Antenna for Wireless Applications"

5,0). The dimension of circular patch is 17 mm and dimension of rectangular patch is 12.4×16.59 mm. The ground plane is placed full size on the opposite side of the substrate. The main objective of the overall work is to enhance the bandwidth & minimize the return loss.

The proposed antenna is designed and simulated in High Frequency Structure Simulator (HFSS) software. The patch is operated at different frequencies of 16.6 GHz and 18.3 GHz respectively. The basic schematic of antenna configuration is shown in the figure 1.



Figure 1: Top and Side View of the Proposed Antenna

The performance parameter of the antenna is to be improve are gain, bandwidth and return loss. These parameters are as [21]:

1. Directivity

Directivity is the ratio of the radiation intensity in a given direction from the antenna to the radiation intensity averaged over all directions.

$$\mathbf{D} = \frac{4\pi U}{P_{rad}}$$

2. Gain

Gain of an antenna is the ratio of the intensity, in a given direction, to the radiation intensity that would be obtained if

the power accepted by the antenna were radiated isotropically.

$$Gain = 4\pi \frac{Radiation Intensity}{Total Input (accepted)Power}$$

3. Bandwidth

The bandwidth of an antenna is defined as the range of frequency within the performance of the antenna. The bandwidth of narrow band and broadband antennas are defined as

4. Return loss

Return loss or reflection loss is the reflection of signal power from the insertion of a device in a transmission line. It is expressed as ratio in dB relative to the transmitted signal power. The return loss is given by

$$RL = 10Log \frac{P_r}{P_i}$$

 $B.W = F_h - F_l$

III. Results and Discussion

Now a days it is a common practice to evaluate the system performances through computer simulation before the real time implementation. A simulator "Ansoft HFSS" based on finite-element method has been used to calculate return loss, bandwidth, and gains. This simulator also helps to reduce the fabrication cost because only the antenna with the best performance would be fabricated [1]. Figure 2 shows the simulated results of the return loss of the proposed antenna. After Simulation the return loss is obtained -35.7673 dB with gain 5.6925 dB and bandwidth 563 MHz at 16.6 GHz. Frequency Vs Return Loss is shown in fig 2. A negative value for return loss shows that this antenna had not many losses while transmitting the signals. Antenna is another one operated at 18.3 GHz. After simulation return loss is obtained -29.2070 dB with gain 12.44 dB and bandwidth 268 MHz at 18.3 GHz. Frequency Vs Return Loss is shown in fig 2.





patch antenna that can operate at 16.6 GHz & 18.3 GHz and

Radiation Pattern 1

The simulated results for gain that are obtained from the proposed antenna at 16.6 GHz and 18.3 GHz are shown in figure 3 and figure 4 respectively. These figures show

Mag

Ang

Name

Theta

frequency versus gain graph. The gain is given in variation of theta & phi and range of theta and phi is taken from 0 to 90 degree.

HFSSDesign1



-180 Figure 4: Gain of the Proposed Antenna at 18.3 GHz.

Table 1. Comp	parative analys	sis of Proposed	Antenna & C	Conventional A	Intenna

Sr. No.	Parameter	Base Paper	Proposed Antenna
1.	Return loss	-17.39 db	-35.7673 db
2.	Gain	5.1 db	12.44 db
3.	Bandwidth	40 MHz	563 MHz
4	Resonant frequency	2.4 GHz & 5.5 GHz	16.6 GHz &18.3 GHz

IV. Conclusion

In this paper, a microstrip patch antenna for various wireless applications has been presented. The microstrip patch antenna mostly used in modern wireless communication. The goal of this paper is to design and simulate microstrip

"Design & Simulation of Circular Rectangular Microstrip Patch Antenna for Wireless Applications"

patch antenna that can operate at higher frequency. The proposed antenna works at 16.6 GHz and obtained return loss of -35.7673 dB with gain 5.6925 dB and bandwidth 563 MHz. Antenna is also operated at 18.3 GHz and obtained return loss of -29.2070 dB with gain 12.44 db and bandwidth 268 MHz.

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"Design & Simulation of Circular Rectangular Microstrip Patch Antenna for Wireless Applications"

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